# Introduction

[Intro]

# Architecture Overview

[Overview/Summary]

[High Level Diagram]

## Module Decomposition Table

[Description]

[Module Decomposition Chart]

## Module Decomposition Diagram

[Description]

[Module Decomposition Diagram]

## Module Data Flows Table

[Description]

[Module Data Flows Table]

## Producer Consumer Table

[Description]

[Producer Consumer Table]

## Module Functional Descriptions

### Use Interface Layer Modules

[Module Functional Descriptions]

### Preprocessing Layer Modules

[Module Functional Descriptions]

### Processing Layer Modules

[Module Functional Descriptions]

### Post Processing Layer Modules

[Module Functional Descriptions]

### Physical Layer Modules

[Module Functional Descriptions]

### Printer Feedback Layer Modules

[Module Functional Descriptions]

### Communication Layer Modules

[Module Functional Descriptions]

# User Interface Layer

[Description]

## [Subsystem] Modules

[Description]

### [Module Name]

[Description]

#### Prologue

[Name/Description/Function]

#### Interfaces

|  |  |  |
| --- | --- | --- |
| Interface | Information Required | Information Returned |
|  |  |  |
|  |  |  |
|  |  |  |

#### Process

[Pseudo-code]

# Preprocessing Layer

[Description]

## [Subsystem] Modules

[Description]

### [Module Name]

[Description]

#### Prologue

[Name/Description/Function]

#### Interfaces

|  |  |  |
| --- | --- | --- |
| Interface | Information Required | Information Returned |
|  |  |  |
|  |  |  |
|  |  |  |

#### Process

[Pseudo-code]

# processing Layer

[Description]

## [Subsystem] Modules

[Description]

### [Module Name]

[Description]

#### Prologue

[Name/Description/Function]

#### Interfaces

|  |  |  |
| --- | --- | --- |
| Interface | Information Required | Information Returned |
|  |  |  |
|  |  |  |
|  |  |  |

#### Process

[Pseudo-code]

# Post processing Layer

[Description]

## [Subsystem] Modules

[Description]

### [Module Name]

[Description]

#### Prologue

[Name/Description/Function]

#### Interfaces

|  |  |  |
| --- | --- | --- |
| Interface | Information Required | Information Returned |
|  |  |  |
|  |  |  |
|  |  |  |

#### Process

[Pseudo-code]

# Physical Layer

[Description]

## [Subsystem] Modules

[Description]

### [Module Name]

[Description]

#### Prologue

[Name/Description/Function]

#### Interfaces

|  |  |  |
| --- | --- | --- |
| Interface | Information Required | Information Returned |
|  |  |  |
|  |  |  |
|  |  |  |

#### Process

[Pseudo-code]

# Printer Feedback Layer

[Description]

## [Subsystem] Modules

[Description]

### [Module Name]

[Description]

#### Prologue

[Name/Description/Function]

#### Interfaces

|  |  |  |
| --- | --- | --- |
| Interface | Information Required | Information Returned |
|  |  |  |
|  |  |  |
|  |  |  |

#### Process

[Pseudo-code]

# Communication Layer

[Description]

## [Subsystem] Modules

[Description]

### [Module Name]

[Description]

#### Prologue

[Name/Description/Function]

#### Interfaces

|  |  |  |
| --- | --- | --- |
| Interface | Information Required | Information Returned |
|  |  |  |
|  |  |  |
|  |  |  |

#### Process

[Pseudo-code]

# Quality Assurance

[Description of test plans and procedures]

## Unit Testing

### [Layer Name]

#### [Subsystem Name]

##### [Module Name]

[Description of tests on the module]

## Component Testing

[NOT SURE WHAT EXACTLY THIS IS YET]

## Integration Testing

[Description of integration testing]

## System Verification Testing

[Description of system verification testing (possibly take from SRS)]

## Test Cases

[Description]

|  |  |
| --- | --- |
| Test Case | Expected Result |
|  |  |
|  |  |

# Requirements Traceability Matrix

[Description]

[Requirements traceability matrix (probably want to break this into layers)]

# Acceptance Plan

[Description/Overview]

## Package and Installation

[Probably use requirements from SRS]

## Acceptance Testing

[Description]

## Acceptance Criteria

[Reiterate acceptance criteria for requirements]

# Appendices